



174/285

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : David Lewis et al.  
Application No. : 10/766,464  
Confirmation No. : 3178  
Filed : January 27, 2004  
For : ERROR CORRECTION FOR PROGRAMMABLE  
LOGIC INTEGRATED CIRCUITS  
Group Art Unit : 2184

New York, New York 10020  
October 6, 2004

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

TRANSMITTAL LETTER FOR  
INFORMATION DISCLOSURE STATEMENT

Sir:

Transmitted herewith is an Information Disclosure Statement in the above-identified application. This Statement is submitted:

- within three months of the application filing date;
- more than three months from the application filing date but before the mailing date of the first Office Action on the merits.

In accordance with 37 C.F.R. § 1.97, submission of this Statement requires no fee. However, if for any reason a fee is due, the Director is hereby authorized to charge payment of any fees required in connection with this Information Disclosure Statement to Deposit Account

No. 06-1075. A duplicate copy of this letter is  
transmitted herewith.

Respectfully submitted,

Evelyn C. Mak

Evelyn C. Mak  
Registration No. 50,492  
Attorney for Applicants

FISH & NEAVE LLP  
Customer No. 36981  
1251 Avenue of the Americas  
New York, New York 10020-1105  
(212) 596-9000

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Claire J. Sullivan Goodman

Signature  
Signature of Person Signing



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INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97,  
applicants hereby make the following documents of record  
in the above-identified patent application:

U.S. PATENT DOCUMENTS

4,930,098	Allen	May 29, 1990
4,930,107	Chan et al.	May 29, 1990
4,940,909	Mulder et al.	July 10, 1990
5,200,920	Norman et al.	April 6, 1993
5,237,219	Cliff	August 17, 1993
5,291,079	Goetting	March 1, 1994
5,321,704	Erickson et al.	June 14, 1994
5,349,691	Harrison et al.	September 20, 1994
5,426,379	Trimberger	June 20, 1995
5,430,687	Hung et al.	July 4, 1995
5,466,117	Resler et al.	November 14, 1995
5,528,176	Kean	June 18, 1996

U. S. PATENT DOCUMENTS

5,543,730	Cliff et al.	August 6, 1996
5,552,722	Kean	September 3, 1996
5,590,305	Terrill et al.	December 31, 1996
5,598,424	Erickson et al.	January 28, 1997
5,598,530	Nagae	January 28, 1997
5,606,276	McClintock	February 25, 1997
5,608,342	Trimberger	March 4, 1997
5,629,949	Zook	May 13, 1997
5,640,106	Erickson et al.	June 17, 1997
5,650,734	Chu et al.	July 22, 1997
5,670,897	Kean	September 23, 1997
5,680,061	Veenstra et al	October 21, 1997
5,691,907	Resler et al.	November 25, 1997
5,694,056	Mahoney et al.	December 2, 1997
5,694,399	Jacobson et al.	December 2, 1997
5,696,454	Trimberger	December 9, 1997
5,742,531	Freidin et al.	April 21, 1998
5,767,734	Vest et al.	June 16, 1998
5,773,993	Trimberger	June 30, 1998
5,798,656	Kean	August 25, 1998
5,821,772	Ong et al.	October 13, 1998
5,838,167	Erickson et al.	November 17, 1998
5,844,829	Freidin et al.	December 1, 1998
5,844,854	Lee	December 1, 1998
5,869,980	Chu et al.	February 9, 1999
5,873,113	Rezvani	February 16, 1999
5,949,987	Curd et al.	September 7, 1999
5,961,576	Freidin et al.	October 5, 1999
5,995,744	Guccione	November 30, 1999
5,995,988	Freidin et al.	November 30, 1999
5,999,014	Jacobson et al.	December 7, 1999
6,011,406	Veenstra	January 4, 2000
6,018,250	Chiang et al.	January 25, 2000
6,023,565	Lawman et al.	February 8, 2000
6,028,445	Lawman	February 22, 2000
6,044,025	Lawman	March 28, 2000
6,049,222	Lawman	April 11, 2000
6,052,755	Terrill et al.	April 18, 2000
6,057,704	New et al.	May 2, 2000
6,069,489	Iwanczuk et al.	May 30, 2000

U.S. PATENT DOCUMENTS

6,097,210	Iwanczuk et al.	August 1, 2000
6,105,105	Trimberger	August 15, 2000
6,128,215	Lee	October 3, 2000
6,137,307	Iwanczuk et al.	October 24, 2000
6,154,048	Iwanczuk et al.	November 28, 2000
6,184,705 B1	Cliff et al.	February 6, 2001
6,191,614 B1	Schultz et al.	February 20, 2001
6,201,406 B1	Iwanczuk et al.	March 13, 2001
6,204,687 B1	Schultz et al.	March 20, 2001
6,216,259 B1	Guccione et al.	April 10, 2001
6,237,124 B1	Plants	May 22, 2001
6,242,941 B1	Vest et al.	June 5, 2001
6,314,550 B1	Wang et al.	November 6, 2001
6,429,682 B1	Schultz et al.	August 6, 2002
6,560,743 B2	Plants	May 6, 2003

U.S. PUBLISHED APPLICATION

2003/0097628 A1 Ngo et al. May 22, 2003

FOREIGN DOCUMENTS

EP 0 291 167 A2	EPO	November 17, 1988
EP 0 838 969 A2	EPO	April 29, 1998
EP 1 100 020 A1	EPO	May 16, 2001

OTHER DOCUMENTS

The Programmable Logic Data Book 1999, Xilinx,  
pp. 4-31 to 4-37, 6-49 to 6-57, and 7-104 to 7-113.

Application Note: FPGAS, "Correcting Single-  
Event Upsets Through Virtex Partial Configuration,"  
Xilinx, XAPP216 (v1.0), June 1, 2000, pp. 1-12.

Application Note: Virtex Series, "Virtex FPGA  
Series Configuration and Readback," Xilinx, XAPP138  
(v2.7), July 11, 2002, pp. 1-39.

Copies of the aforementioned documents, which are listed on the accompanying Form PTO-1449 (submitted in duplicate), are enclosed herewith.

It is respectfully requested that these documents be (1) fully considered by the Patent and Trademark Office during the examination of this application; and (2) printed on any patent which may issue on this application. Applicants request that a copy of Form PTO-1449, as considered and initialed by the Examiner, be returned with the next communication.

Consideration of the foregoing in relation to this patent application is respectfully requested.

Respectfully submitted,

Evelyn C. Mak

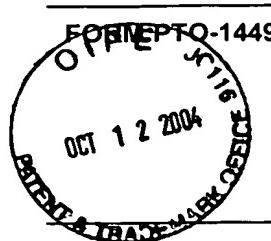
Evelyn C. Mak  
Registration No. 50,492  
Attorney for Applicants

FISH & NEAVE LLP  
Customer No. 36981  
1251 Avenue of the Americas  
New York, New York 10020-1105  
(212) 596-9000

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Claire J. Schild van Goodman

J. van S.  
Signature of Person Signing


**U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE**
**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANTS**
**ATTY. DOCKET NO.  
174/285**
**APPLICATION NO.  
10/766,464**
**APPLICANTS**

David Lewis et al.

**CONFIRMATION NO.  
3178**
**FILING DATE**

January 27, 2004

**GROUP  
2184**
**U.S. PATENT DOCUMENTS**

<b>EXAMINER INITIAL</b>	<b>DOCUMENT NUMBER</b>	<b>DATE</b>	<b>NAME</b>	<b>CLASS</b>	<b>SUBCLASS</b>	<b>FILING DATE IF APPROPRIATE</b>
	4,930,098	05/29/90	Allen	364	716	
	4,930,107	05/29/90	Chan et al.	365	189.08	
	4,940,909	07/10/90	Mulder et al.	307	465	
	5,200,920	04/06/93	Norman et al.	365	185	
	5,237,219	08/17/93	Cliff	307	465	
	5,291,079	03/01/94	Goetting	307	465	
	5,321,704	06/14/94	Erickson et al.	371	37.1	
	5,349,691	09/20/94	Harrison et al.	395	800	
	5,426,379	06/20/95	Trimberger	326	39	
	5,430,687	07/04/95	Hung et al.	365	230.08	
	5,466,117	11/14/95	Resler et al.	414	799	
	5,528,176	06/18/96	Kean	326	105	
	5,543,730	08/06/96	Cliff et al.	326	38	
	5,552,722	09/03/96	Kean	326	41	
	5,590,305	12/31/96	Terrill et al.	395	430	
	5,598,424	01/28/97	Erickson et al.	371	48	
	5,598,530	01/28/97	Nagae	395	182.19	
	5,606,276	02/25/97	McClintock	327	263	
	5,608,342	03/04/97	Trimberger	326	38	
	5,629,949	05/13/97	Zook	371	37.1	
	5,640,106	06/17/97	Erickson et al.	326	38	
	5,650,734	07/22/97	Chu et al.	326	38	
	5,670,897	09/23/97	Kean	326	41	
	5,680,061	10/21/97	Veenstra et al.	326	38	
	5,691,907	11/25/97	Resler et al.	364	468.28	
	5,694,056	12/02/97	Mahoney et al.	326	38	
	5,694,399	12/02/97	Jacobson et al.	371	22.3	

**EXAMINER****DATE CONSIDERED**

**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not conformance and not considered. Include copy of this form with next communication to applicants.

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 174/285	APPLICATION NO. 10/766,464
INFORMATION DISCLOSURE STATEMENT BY APPLICANTS		APPLICANTS David Lewis et al.		CONFIRMATION NO. 3178
		FILING DATE January 27, 2004		GROUP 2184

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,696,454	12/09/97	Trimberger	326	38	
	5,742,531	04/21/98	Freidin et al.	364	716.03	
	5,767,734	06/16/98	Vest et al.	327	536	
	5,773,993	06/30/98	Trimberger	326	38	
	5,798,656	08/25/98	Kean	326	39	
	5,821,772	10/13/98	Ong et al.	326	38	
	5,838,167	11/17/98	Erickson et al.	326	38	
	5,844,829	12/01/98	Freidin et al.	364	716.03	
	5,844,854	12/01/98	Lee	361	230.01	
	5,869,980	02/09/99	Chu et al.	326	38	
	5,873,113	02/16/99	Rezvani	711	103	
	5,949,987	09/07/99	Curd et al.	395	500.17	
	5,961,576	10/05/99	Freidin et al.	708	232	
	5,995,744	11/30/99	Guccione	395	500.44	
	5,995,988	11/30/99	Freidin et al.	708	232	
	5,999,014	12/07/99	Jacobson et al.	326	38	
	6,011,406	01/04/00	Veenstra	326	38	
	6,018,250	01/25/00	Chiang et al.	326	38	
	6,023,565	02/08/00	Lawman et al.	395	500.02	
	6,028,445	02/22/00	Lawman	326	38	
	6,044,025	03/28/00	Lawman	365	191	
	6,049,222	04/11/00	Lawman	326	38	
	6,052,755	04/18/00	Terrill et al.	711	103	
	6,057,704	05/02/00	New et al.	326	38	
	6,069,489	05/30/00	Iwanczuk et al.	326	40	
	6,097,210	08/01/00	Iwanczuk et al.	326	39	
	6,105,105	08/15/00	Trimberger	711	103	

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6,128,215	10/03/00	Lee	365	154	
	6,137,307	10/24/00	Iwanczuk et al.	326	38	
	6,154,048	11/28/00	Iwanczuk et al.	326	38	
	6,184,705 B1	02/06/01	Cliff et al.	326	38	
	6,191,614 B1	02/20/01	Schultz et al.	326	41	
	6,201,406 B1	03/13/01	Iwanczuk et al.	326	38	
	6,204,687 B1	03/20/01	Schultz et al.	326	40	
	6,216,259 B1	04/10/01	Guccione et al.	716	17	
	6,237,124 B1	05/22/01	Plants	714	763	
	6,242,941 B1	06/05/01	Vest et al.	326	26	
	6,314,550 B1	11/06/01	Wang et al.	716	17	
	6,429,682 B1	08/06/02	Schultz et al.	326	41	
	6,560,743 B2	05/06/03	Plants	714	763	

**U.S. PATENT APPLICATION**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	2003/0097628 A1	05/22/03	Ngo et al.			

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	EP 0 291 167 A2	11/17/88	EPO	G11B	20/18		
	EP 0 838 969 A2	04/29/98	EPO	H04Q	11/04		
	EP 1 100 020 A1	05/16/01	EPO	G06F	12/16		

**EXAMINER****DATE CONSIDERED**

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANTS</b>		<b>APPLICANTS David Lewis et al.</b>	<b>CONFIRMATION NO. 3178</b>
		<b>FILING DATE January 27, 2004</b>	<b>GROUP 2184</b>

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

EXAMINER INITIAL	
	<p>The Programmable Logic Data Book 1999, Xilinx, pp. 4-31 to 4-37, 6-49 to 6-57, and 7-104 to 7-113.</p>
	<p>Application Note: FPGAS, "Correcting Single-Event Upsets Through Virtex Partial Configuration," Xilinx, XAPP216 (v1.0), June 1, 2000, pp. 1-12.</p>
	<p>Application Note: Virtex Series, "Virtex FPGA Series Configuration and Readback," Xilinx, XAPP138 (v2.7), July 11, 2002, pp. 1-39.</p>

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